

FIG. 1

FIG. 2

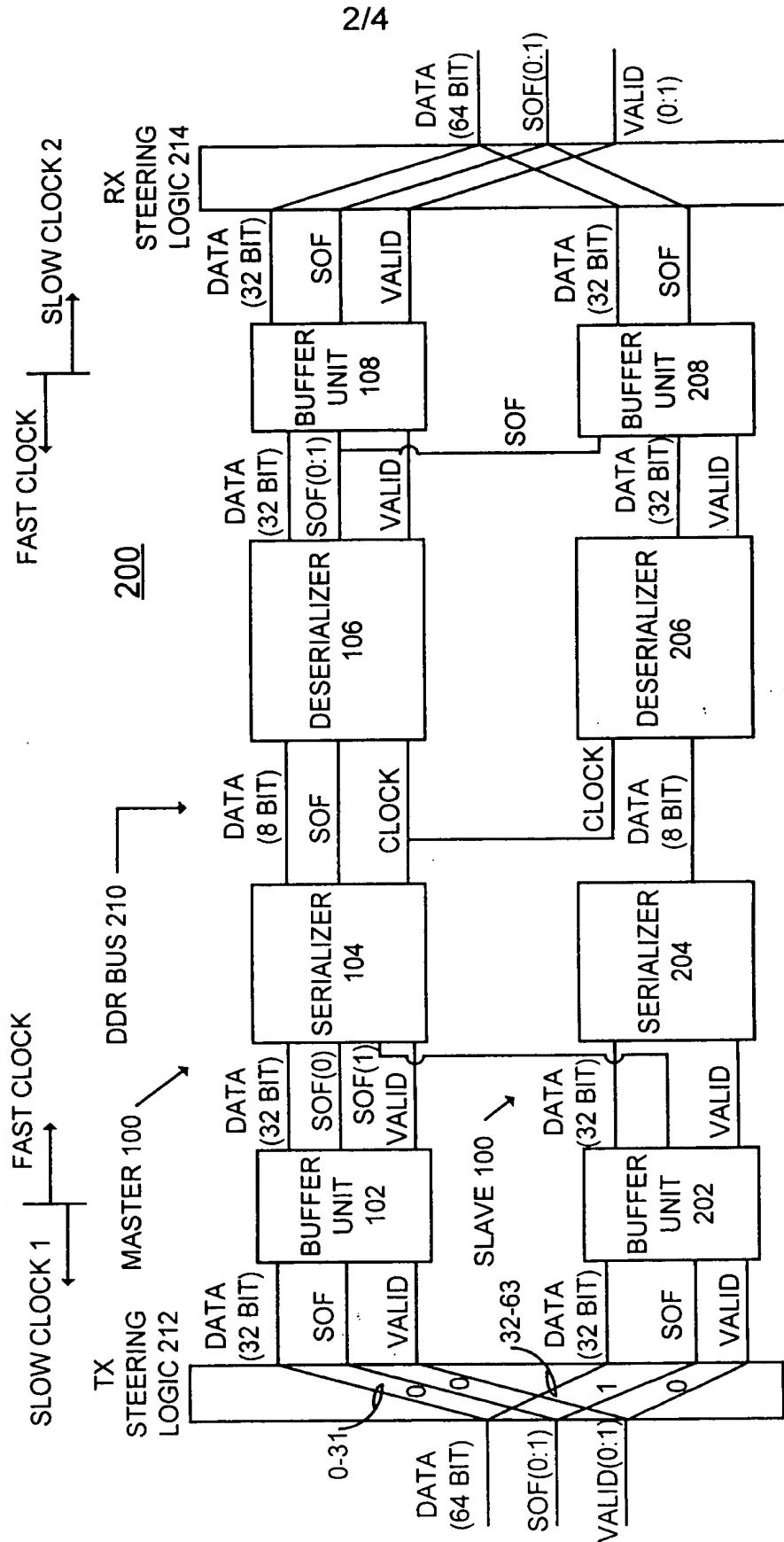


FIG. 3

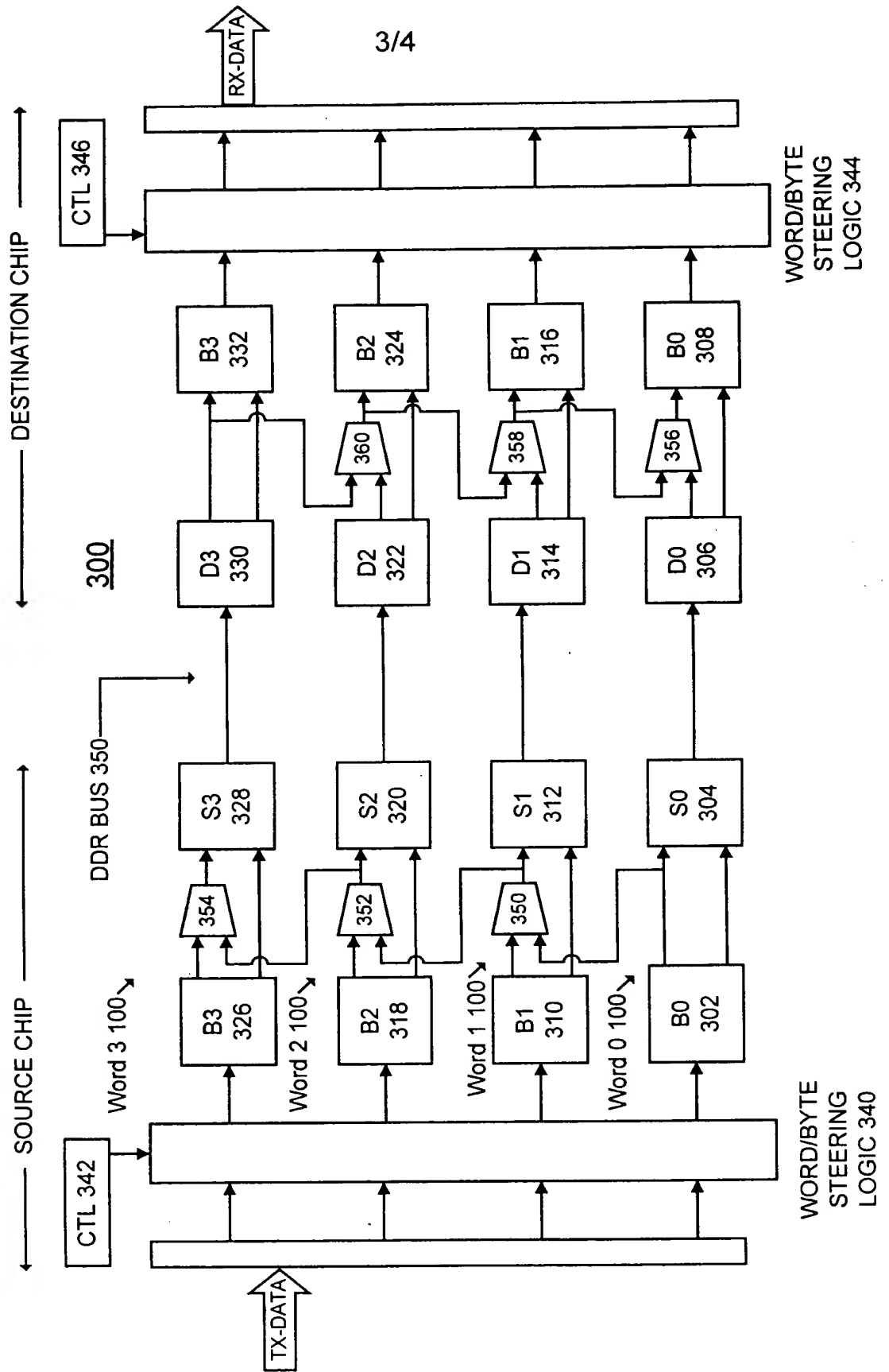


FIG. 4

SPLIT-BUS CONFIGURATIONS

400

WORDS 402	MODE 404	WORD 3..0 MASTER/SLAVE 406	AVAIL/VALID 408
4	32-BIT	M/S/S/S	3,X,X,X
4	16-BIT X 2	M/S/M/S	3,X,1,X
4	8-BIT X 4	M/M/M/M	3,2,1,0
4	16-BIT, 8-BIT X 2	M/S/M/M	3,X,1,0
4	8-BIT X 2, 16-BIT	M/M/M/S	3,2,1,X
2	16-BIT	M/S	-,-,1,X
2	8-BIT X 2	M/M	-,-,1,0
1	8-BIT	M	-,-,-,0